

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7487	((multiple or multi\$4 or plurality or group or number) near5 (process\$4 cpu host device node cluster)) same (coherency or coheren\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:11
L2	4	((coherency coheren\$4) same ownership same (cach\$3 near4 (line address entry)) same snoop\$4 same flush\$3) and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:03
L3	471	((controller control\$3) near5 (logic device circuit circuitry)) with (prevent\$3 limit\$4 permit\$4 lock\$3)) and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:11
L4	9	(coherency coheren\$4) same ownership same (cach\$3 near4 (line address entry)) and snoop\$4 and flush\$3 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:11
L6	2708701	((multiple or multi\$4 or plurality or group or number) with (process\$4 cpu host device node cluster))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:11
L7	147	((controller control\$3) near5 (logic device circuit circuitry)) same (prevent\$3 limit\$4 permit\$4 lock\$3)) and ((coheren\$4 or coherency) same (ownership with (line entry address data)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:12
L8	31	snoop\$4 and flush\$3 and 6 and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:13
L9	24	(instruct\$3 operand) and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:15
L12	3	partition and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:14

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7487	((multiple or multi\$4 or plurality or group or number) near5 (process\$4 cpu host device node cluster)) same (coherency or coheren\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:11
L2	4	((coherency coheren\$4) same ownership same (cach\$3 near4 (line address entry)) same snoop\$4 same flush\$3) and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:03
L3	471	((((controller control\$3) near5 (logic device circuit circuitry)) with (prevent\$3 limit\$4 permit\$4 lock\$3)) and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:11
L4	9	(coherency coheren\$4) same ownership same (cach\$3 near4 (line address entry)) and snoop\$4 and flush\$3 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:11
L6	2708701	((multiple or multi\$4 or plurality or group or number) with (process\$4 cpu host device node cluster))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:11
L7	147	((((controller control\$3) near5 (logic device circuit circuitry)) same (prevent\$3 limit\$4 permit\$4 lock\$3)) and ((coheren\$4 or coherency) same (ownership with (line entry address data))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:47
L8	31	snoop\$4 and flush\$3 and 6 and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:33
L9	24	(instruct\$3 operand) and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:34
L13	190	((((controller control\$3) near5 (logic device circuit circuitry)) same (prevent\$3 limit\$4 permit\$4 lock\$3)) and ((coheren\$4 or coherency) same ownership)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:33

L14	42	snoop\$4 and flush\$3 and 6 and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:34
L15	35	(instruct\$3 operand) and 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:43
L16	266025	((lock\$3 prevent\$3 permit\$4 limit\$4) with (signal instruct\$3 operand command)) and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:46
L17	46	((coheren\$4 or coherency) same (ownership with (line entry address data))) and snoop\$4 and flush\$3 and 16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:48
L18	7	partition and 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:49
L19	7	(suspend\$3 suspension) and 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/12/07 17:49